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APPLICANTS Atsushi Sano, Toyama, JAPAN; [S/JN] Sadayoshi Horii, Toyama, JAPAN; Hideharu Itatani, Toyama, JAPAN; Masayuki Asai, Toyama, JAPAN;						
** CONTINUING DATA ***** This application is a 371 of PCT/JP05/00751 01/21/2005 [S/JN]						
** FOREIGN APPLICATIONS ***** JAPAN 2004-012885 01/21/2004 [S/JN]						
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Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	/S/JN/	JAPAN	16	21	4
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Acknowledged	NIKMANESH/					
ADDRESS OLIFF & BERRIDGE, PLC P.O. BOX 320850 ALEXANDRIA, VA 22320-4850 UNITED STATES						
TITLE Manufacturing method of semiconductor device and substrate processing apparatus						
FILING FEE RECEIVED 1150	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees		
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